Impedance Based Resonance Analysis in a VSC-HVDC System

Ling Xu, Student Member, IEEE, and Lingling Fan, Senior Member, IEEE

Abstract—Resonances can limit power transfer in a Voltage Source Converter-High Voltage DC (VSC-HVDC) system. The objective of this paper is to develop impedance models for the rectifier AC system and the inverter AC system for a VSC-HVDC system. Resonance stability will be examined using Nyquist stability criterion and impedance frequency responses. The impedance models considers the outer control loop and the inner current control loops of VSCs. Impacting factors are then examined. Stability analysis demonstrates that the feed-forward filter, line length and power transfer level have significant impact on resonances. Time-domain simulation results obtained Matlab SimPowerSystems are used to validate the analysis.

Index Terms—Admittance, impedance, resonance stability, VSC-HVDC.

I. INTRODUCTION

VSC-HVDC is becoming an attractive solution to deliver renewable energy to main grids [1–3]. Harmonic resonances can impact the power quality and the power transfer level. Normally, harmonics generated by switching sequences could be eliminated by AC filters. However, there are low frequency harmonic resonances due to the interaction of AC grid and VSC controllers. Those resonances are not easy to be filtered out. Though harmonic resonances have been examined in VSC interfaced AC grids in [4–7], a comprehensive examination of the both AC systems (the rectifier side and the inverter side) has not been seen in the literature. It will be the objective of this paper.

Since harmonic resonances are related to electrical systems mainly, impedance or admittance model based analysis is feasible in understanding the phenomena. In [6–8], impedance models of grid-connected inverters are developed for resonance analysis. [6, 8] developed impedances models of converters with only the inner current control loops considered. The outer control loops are ignored. Investigation shows that grid inductor, shunt capacitor and phase locked loop (PLL) have impact on harmonic resonances.

Impedance models of the VSC converter with inner current control and outer DC/AC voltage control loops are developed in [7]. Resonance stability is studied for both power flow direction. The studied AC grid is represented by a series RL circuit. It is stated in [7] that for the interaction of a converter and the grid impedance, with outer loop ignored, instability cannot be detected. With the outer control loop and PLL considered, instability can be detected. Therefore, modeling of the outer control loops is important.

In a VSC-HVDC system, there are two converters and each has its own control strategies. The rectifier controls power flow while the inverter controls DC-link voltage [9], [7] studied only the interactions of the inverter and AC grid. The paper also assumes that the integral coefficients of converter proportional integral (PI) controllers are equal to zero. In many literatures, the integral gains are greater than the proportional gains [10, 11].

In this paper, resonance stability will be investigated for the two AC systems (the rectifier side AC grid and the inverter side AC grid). Typical VSC-HVDC controls will be adopted and impedance models of the systems will be developed with the inner and outer converter control loops included. Nyquist stability criterion and impedance frequency responses will then be applied to detect resonances. Impacting factors on stability such as feed-forward filter structure, line length and power transfer levels will be identified.

In the sections follow, the paper first describes the system topology of a VSC-HVDC system in Section II. The impedance and admittance models are presented. Analysis and simulations to evaluate resonances are carried out in Section III. The paper is concluded in Section IV.

II. SYSTEM MODEL

A VSC-HVDC system is presented in Fig. 1, where the basic controller for the rectifier and inverter stations are also presented. Normally, power is sent from the rectifier station to the inverter station. Power control is implemented at the rectifier station, while the inverter station controls DC-link voltage to ensure power balance. An advantage of VSC-HVDC over Line-Commutated Converters HVDC (LCC-HVDC) is its reactive power compensation capability, which is provided by the reactive power controller or AC voltage controller.

For a balanced three-phase system, Fig. 2 shows the equivalent circuit of a VSC connecting with an AC grid via a coupling inductor $L$, where the resistor is neglected. The AC grid is modeled as an AC voltage source $v$, with an impedance $Z(s)$. (1) describes the voltage and current relationship between grid and converter in time domain and represented in the $dq$ frame. The letter with upper-line represents complex space vector, e.g., $\bar{v} = v_d + jv_q$ and $\bar{i} = i_d + ji_q$. The angular speed within grid reference frame is $\omega_1$, which is a constant throughout this paper.

$$L \frac{d\bar{i}}{dt} + j\omega_1 L \bar{i} = \bar{E} - \bar{v}$$

(1)

where $E$ and $v$ are the point of common coupling (PCC) voltage and the converter output voltage.
A. Impedance Models

1) Rectifier Station:

a) Current Control: The VSC-HVDC system consists of two terminal stations, one is the rectifier station and the other is the inverter station. Fig. 3 presents the controller of the rectifier station, in which active power \( P_{\text{ref}} \) and reactive power \( Q_{\text{ref}} \) are to be regulated.

\[
\begin{align*}
\nu_{\text{ref}} &= -(k_p + \frac{k_i}{s})(\nu_{\text{ref}} - \bar{\nu}) - j\omega_1 L\bar{\nu} \\
&+ \frac{\omega_0^2}{s^2 + 2\xi\omega_0 s + \omega_0^2} E 
\end{align*}
\] (2)

The feed-forward items \( E_d \) and \( E_q \) are the PCC voltage measured in the \( dq \) frame and obtained through a filter. A second-order filter is included in (2), and the transfer function is shown in (3). Another option is first order filter, and the comparison of the two filters on resonance stability is conducted in Section III.

\[
F_{2nd}(s) = \frac{\omega_0^2}{s^2 + 2\xi\omega_0 s + \omega_0^2} 
\] (3)

where \( \omega_0 \) is the cut-off frequency (1000 Hz) and \( \xi \) is the damping factor (\( \frac{1}{\sqrt{2}} \)).

The controller utilizes PI controllers:

\[
F_c(s) = k_p + \frac{k_i}{s}. 
\] (4)

Rearranging (2) leads to (5),

\[
\bar{\nu}^c = g_c(s)\nu_{\text{ref}} + y_i(s)E^c 
\] (5)

where

\[
\begin{align*}
g_c(s) &= \frac{k_s + k_i}{L_s s^2 + k_p s + k_i} \\
y_i(s) &= \frac{(L_s s^2 + 2\xi\omega_0 s + \omega_0^2)}{(L_s^2 s^4 + 2k_p s^3 + k_i s^2 + 2\xi\omega_0 s + \omega_0^2)}
\end{align*}
\] (6)

(5) describes the relationship of the current \( \bar{\nu}^c \) in terms of \( \nu_{\text{ref}} \) and \( E^c \). Alternatively, (5) could be written in matrix form (7).

\[
\bar{\nu}^c = \begin{bmatrix} g_c(s) & 0 \\ 0 & g_c(s) \end{bmatrix} \nu_{\text{ref}} + \begin{bmatrix} y_i(s) & 0 \\ 0 & y_i(s) \end{bmatrix} E^c 
\] (7)

where \( \mathbf{f} = [f_d, f_q]^T \) and \( \mathbf{f} \) stands for current or voltage symbol.
b) Power Control: The rectifier station controls the active power transferred from the left-hand side grid to the converter, which is the outer control loop in dual dq control loops. Equation (8) computes the d axis current reference that controls the active power flow.

\[
i_{\text{dref}} = \frac{P_{\text{ref}}}{E_d} = \frac{P_{\text{ref}}}{E_f}
\]  

(8)

where \( E_f \) is the d-axis PCC voltage after a filter.

\[
E_f = F_{2nd}(s) |E|
\]  

(9)

The small signal representation of \( d \) axis current reference is then expressed as follows.

\[
\Delta i_{\text{dref}} = -\frac{P_{\text{dref}}}{E_d^2} F_{2nd}(s) \Delta E_d
\]  

(10)

where \( E_0 \) is the rectifier PCC steady-state voltage. Since the direct reactive power compensation control is used throughout this paper, the \( q \) axis current reference can be derived similarly as above.

\[
\begin{align*}
\Delta i_{\text{qref}} &= -\frac{Q_{\text{ref}}}{E_d^2} - \frac{Q_{\text{ref}}}{E_f^2} \\
\Delta i_{\text{qref}} &= \frac{Q_{\text{ref}}}{E_d^2} F_{2nd}(s) \Delta E_d
\end{align*}
\]  

(11)

Combining (10) and (11) leads to (12).

\[
\Delta i_{\text{ref}} = \left[ -\frac{P_{\text{dref}}}{E_d^2} F_{2nd}(s) \quad 0 \right] \Delta E + G_{\text{ref},i_{\text{dref}}}(s)
\]  

(12)

The input admittance of \( \Delta E \) and \( \Delta i \) is then expressed as.

\[
Y_{\text{rec}}(s) = Y_I(s) + G_{c}(s) G_{\text{Ei}}(s) = \left[ \begin{array}{cc} y_I(s) - \frac{P_{\text{dref}}}{E_d^2} F_{2nd}(s) g_c(s) & 0 \\
\frac{Q_{\text{ref}}}{E_d^2} F_{2nd}(s) g_c(s) & y_I(s) \end{array} \right]
\]  

(13)

2) Inverter Station:

a) DC Voltage Control: The inverter station regulates the DC-link voltage and maintains the active power balance. The controller is depicted in Fig. 4, which consists of two control loops. The inner current control loop is identical to the rectifier station shown in Fig. 3, whereas the outer loop is replaced by a DC voltage controller. The DC voltage controller \( F_{\text{dc}}(s) \) is a typical \( PI \) controller shown in (14).

\[
F_{\text{dc}}(s) = k_{\text{pdc}} + \frac{k_{\text{idc}}}{s}
\]  

(14)

A feed-forward item \(-\frac{P_{\text{ref}, \text{rec}}}{E_{\text{d, inv}}} \) is applied to improve the controller dynamic performance. Hence, the d-axis current reference could be established as follows.

\[
i_{\text{dref}} = (k_{\text{pdc}} + \frac{k_{\text{idc}}}{s})(v_{\text{dref}} - v_{\text{dc}}) - \frac{P_{\text{ref}, \text{rec}}}{E_{\text{d, inv}}}
\]  

(15)

The small signal model of the \( d \)-axis current reference is expressed in (16).

\[
i_{\text{dref}0} + \Delta i_{\text{dref}} = (k_{\text{pdc}} + \frac{k_{\text{idc}}}{s})(v_{\text{dref}0} - v_{\text{dc}0} - \Delta v_{\text{dc}}) - \frac{P_{\text{ref}, \text{rec}}}{E_{\text{d, inv}}}
\]  

(16)

It can be assumed that \( i_{\text{dref}0} = -\frac{P_{\text{dref}, \text{rec}}}{E_{\text{d, inv}}} \), since the active power delivered by inverter station is the same amount of rectifier station sent if power loss is neglected. The linear model (17) is then obtained.

\[
\Delta i_{\text{dref}} = -\left( k_{\text{pdc}} + \frac{k_{\text{idc}}}{s} \right) \Delta v_{\text{dc}}
\]  

(17)

In order to derive \( \Delta v_{\text{dc}} \), the complex power \( S \) has to be calculated.

\[
S = \left\{ \begin{array}{c}
\overrightarrow{E}\overrightarrow{I} \\
\overrightarrow{E}_0 \end{array} \right\} = (E_0 + \Delta E_d + j \Delta E_q)[i_{\text{d}0} + \Delta i_d - j(i_{\text{q}0} + \Delta i_q)]
\]  

\[
\approx E_0 i_{\text{d}0} + E_0 \Delta i_d + i_{\text{d}0} \Delta E_d + i_{\text{q}0} \Delta E_q + j(-E_0 i_{\text{q}0} - E_0 \Delta i_q + i_{\text{d}0} \Delta E_d - i_{\text{q}0} \Delta E_d)
\]  

(18)

The active power and reactive power will then extracted:

\[
\begin{align*}
P &= E_0 i_{\text{d}0} + E_0 \Delta i_d + i_{\text{d}0} \Delta E_d + i_{\text{q}0} \Delta E_q \\
Q &= -E_0 i_{\text{q}0} - E_0 \Delta i_q + i_{\text{d}0} \Delta E_d - i_{\text{q}0} \Delta E_d
\end{align*}
\]  

(19)

Assuming that \( P_0 = E_0 i_{\text{d}0} \) and \( Q_0 = -E_0 i_{\text{q}0} \), the small deviation parts of powers are then expressed in (20).

\[
\begin{align*}
\Delta P &= E_0 \Delta i_d + i_{\text{d}0} \Delta E_d + i_{\text{q}0} \Delta E_q \\
\Delta Q &= -E_0 \Delta i_q + i_{\text{d}0} \Delta E_d - i_{\text{q}0} \Delta E_d
\end{align*}
\]  

(20)

Considering the dynamics on DC capacitor \( C_{\text{dc}} \), the energy stored in the capacitor is

\[
W = \frac{1}{2} C_{\text{dc}} v_{\text{dc}}^2
\]  

\[
\Rightarrow \frac{1}{2} C_{\text{dc}} \frac{dv_{\text{dc}}}{dt} = P_{\text{dc}} = P - P_L
\]  

(21)

\[
\Rightarrow C_{\text{dc}} \frac{dv_{\text{dc}}}{dt} = P - P_L
\]

where \( P_L \) is the active power consumed by load. Applying small perturbation leads to (22), the linear relationship between DC voltage and active power is obtained.

\[
C_{\text{dc}}(v_{\text{dc}0} + \Delta v_{\text{dc}}) \frac{d(v_{\text{dc}0} + \Delta v_{\text{dc}})}{dt} = P_0 + \Delta P - P_{L0} - \Delta P_L
\]  

\[
\Rightarrow C_{\text{dc}} v_{\text{dc}0} \frac{d\Delta v_{\text{dc}}}{dt} = \Delta P - \Delta P_L
\]  

(22)
Since the current control loop of both inverter and rectifier station is identical, substituting (17) into (5) modifies equation (20) as:

$$\Delta P = E_0[ -g_c(s)F_{dc}(s)\Delta v_{dc} + y_i(s)\Delta E_d] + i_{q0}\Delta E_d + i_{q0}\Delta E_q$$ \hspace{1cm} (23)

Using (22) and (23), and assuming $\Delta P_L = 0$, $\Delta v_{dc}$ is derived in terms of $\Delta E_d$ and $\Delta E_q$.

$$sC_{dc}v_{dc0}\Delta v_{dc} = -E_0g_c(s)F_{dc}(s) + E_0y_i(s)\Delta E_{d.inv}$$

$$+ \frac{P_0}{E_0}\Delta E_d - \frac{Q_0}{E_0}\Delta E_q$$

$$\Rightarrow \Delta v_{dc} = \frac{(E_0^2y_i(s) + P_0)E_{dc} - Q_0\Delta E_q}{sE_0C_{dc}v_{dc0} + E_0^2g_c(s)F_{dc}(s)}$$ \hspace{1cm} (24)

Substituting (24) into (17), $\Delta i_{d.ref}$ could be represented in terms of $\Delta E_d$ and $\Delta E_q$.

$$\Delta i_{d.ref} = \frac{-E_0^2y_i(s) + P_0)F_{dc}(s)}{sE_0C_{dc}v_{dc0} + E_0^2g_c(s)F_{dc}(s)} \Delta E_d$$

$$+ \frac{G_{dc}^d(s)}{sE_0C_{dc}v_{dc0} + E_0^2g_c(s)F_{dc}(s)} \Delta E_q$$ \hspace{1cm} (25)

Besides DC-link voltage control, the inverter station could also compensate reactive power to the right-hand side grid, which utilizes the same control structure as that of the rectifier station. Therefore, the input admittance for the inverter station is shown in equation (26),

$$Y_{inv}(s) = Y_i(s) + G_{E_i.inv}(s)G_{c}(s)$$

$$= \begin{bmatrix} y_i(s) + G_{dc}^d(s)g_c(s) & G_{dc}^a(s)g_c(s) \\ \frac{Q_{dc}}{E_0}F_{2nd}(s)g_c(s) & y_i(s) \end{bmatrix}$$ \hspace{1cm} (26)

where

$$G_{E_i.inv}(s) = \begin{bmatrix} G_{dc}^d(s) & G_{dc}^a(s) \\ \frac{Q_{dc}}{E_0}F_{2nd}(s) & 0 \end{bmatrix}$$ \hspace{1cm} (27)

B. Stability Analysis

Fig. 5. Small-signal representation of a converter-grid system.

The grid is modeled as a combination of an ideal AC voltage source $v_s$ and an impedance $Z_g$, which consists of a resistor $R_g$ and an inductor $L_g$. Under dq frame, the impedance is expressed as $Z_g(s)$ in (28).

$$Z_g = \begin{bmatrix} R_g + sL_g & -\omega L_g \\ \omega L_g & R_g + sL_g \end{bmatrix}$$ \hspace{1cm} (28)

Based on Fig. 5, the current $I$ flowing from the grid to the converter is

$$I(s) = \frac{V_g(s) - Y^{-1}(s)L(s)}{Z_g(s) + Y^{-1}(s)}$$ \hspace{1cm} (29)

which can be rearranged to

$$I(s) = \frac{[Y(s)V_g(s) - L(s)]}{1 + Y(s)Z_g(s)}$$ \hspace{1cm} (30)

Therefore, the stability analysis of the system relies on $Y(s)Z_g(s)$ in (30). Consequently, the grid-connected converter will operate stably if $Y(s)Z_g(s)$ satisfies Nyquist stability criterion. Since the VSC-HVDC system consists of a rectifier station and an inverter station, and the input admittance of each station depends on not only component parameters but also different control loops, it is obvious that many factors may affect the resonance stability.

III. IMPACTING FACTORS OF RESONANCE STABILITY

In this section, we carry out frequency domain analysis based on impedance models and validate the analysis via Matlab/SimPowerSystems simulation. SimPowerSystems is a toolbox of Matlab/Simulink developed by Hydro-Québec of Montreal [12]. Models built in SimPowerSystems include switching details and are considered high-fidelity models. [13] compared the simulation results between SimPowerSystems toolbox and PSPICE, and demonstrated that SimPowerSystems is well suitable for the electrical circuits simulation containing switching devices because it can detect precise discontinuities.

In the literature, SimPowerSystems simulation results substituting experimental results are used for validation for VSC-HVDC systems. For example, [14] compared the simulations of CIGRÉ HVDC Benchmark System between SimPowerSystems and PSCAD/EMTDC, which demonstrated that both simulation tools are accurate and consistent during steady-state and transients situations. [15–17] investigated the various issues of VSC-HVDC using SimPowerSystems toolbox, including subsynchronous resonance, robust sliding-mode Controller, and Small-signal stability analysis.

Therefore, Matlab/SimPowerSystems simulation is also used as a tool to validate the impedance model based analysis.

A. Feed-forward Filter

The feed forward filter in the current control loop as shown in (3) has impact on the input admittance $Y_{rec}(s)$ and hence can influence resonance stability. $E_d$ and $E_q$ are the feed forward PCC voltages in dq frame after the filter. In this subsection, the impact on stability of a second-order filter is compared with a first order filter. The transfer function of a first order low pass filter is as follows:

$$F_{1st}(s) = \frac{1}{s\tau + 1}.$$ \hspace{1cm} (31)
where $\tau$ is the time constant (0.001 s.). Two scenarios with same power transfer level (100 MW) and same inverter control structures are examined:

- The feed-forward filter at the rectifier is a 1st order filter
- The feed-forward filter at the rectifier is a 2nd order filter

The line inductance $L_g$ is chosen to be 0.05 H.

Figs. 6 and 7 present the real part of the total impedances for the rectifier side AC system and the inverter side AC system. Analysis of complex torque coefficients method in [18] indicated that the resonance frequencies are related to the roots of the real part of a transfer function. A brief explanation is offered as follows.

Assuming there are only oscillatory modes in the system, then $Z_g(s) + Z_{conv}(s)$ ($Z_{conv} = Y^{-1}$) should only have roots appearing as complex conjugate pairs ($-\sigma_i \pm \omega_i$). $Z_g(s)$ and $Z_{conv}(s)$ are rational functions. Therefore

$$Z_g(s) + Z_{conv}(s) = \frac{P(s)}{Q(s)} \tag{32}$$

where $P(s)$ and $Q(s)$ are polynomials of $s$

$$P(j\omega) = \gamma \prod_{l=1}^{n} (j\omega + \sigma_l + j\omega_l)(j\omega + \sigma_l - j\omega_l)$$

$$= \gamma \prod_{l=1}^{n} (\sigma_l^2 + \omega^2 - \omega^2 + 2j\sigma_l\omega) \tag{33}$$

where $\gamma$ is a constant.

Assuming negligible damping terms, then $P(j\omega)$ is a real function and denoted as $P_R(\omega)$. Hence

$$Re(Z_g(j\omega) + Z_{conv}(j\omega)) = \alpha(\omega)P_R(\omega) \tag{34}$$

where $\alpha(\omega)$ is also a real function.

When $s = j\omega_l$, $P_R(\omega_l) \approx 0$, and hence $Re(Z_g(s) + Z_{conv}(s)) \approx 0$. From the roots of the real part of the total impedances, resonance frequencies can be identified. For example, from Fig. 6, the resonance frequencies are 70 Hz and 190 Hz when 1st order filter or 2nd order filter is employed.

A detailed VSC-HVDC model is built in Matlab SimPowerSystems to verify the analysis. The parameters of the system are listed in Appendix. The HVDC system is operated at 100 MW power transfer level. Fig. 8 shows the rectifier side $d$-axis current with two types of filters, where the oscillation frequency in the case of first-order filter is much lower than that when the 2nd-order filter case. Fig. 10 shows the active power at the rectifier side with two types of filter. Figs. 11 and 13 present the $d$-axis current and active power at the inverter side. Fig. 9 and Fig. 12 shows the $d$-axis PCC voltage at rectifier and inverter stations with different filters. The $d$-axis PCC voltage with first order filter is lower than the $d$-axis PCC voltage with second order filter. Since the rectifier station operates on power control mode, which causes the $d$-axis current with first order filter case higher than the current with second order filter. The inverter station delivers active power transferred from rectifier station to right-hand side grid, therefore, the $d$-axis current with first order filter case is also higher than the second order filter case.
Simulated results also demonstrate more complicated waveforms in d-axis current and active power. The rectifier side AC line inductance is 0.02H while the inverter side line inductance is 0.012H. The resonances cause around 5% ripples in waveforms and do not pose stability issues.

**B. AC Line Length**

The impact of the line length of the rectifier AC side on resonance is studied in this subsection. The inductance of the rectifier AC system is varied to reflect the change of length. First-order feed forward filter is adopted in the current controller of the VSC. The power transfer level is set to 100 MW. The Nyquist curves for the eigenvalues of $YZ_g$ are presented in Fig. 14.

The Nyquist curves shows that with the increase of line length, the more the system is prone to instability. Take the example of $L_g = 0.12H$, the time domain simulation results of the rectifier AC grid currents in dq-axis are shown in Fig. 15. It is clearly that the system has lost stability.
C. Power Level

Power transfer level is indicated in [7] to impact the converter impedance. Hence in this subsection, the impact of power level is investigated. Fig. 16 gives the Nyquist curves for one of the eigenvalues of $YZ_g$ (rectifier AC system) for two power levels (100 MW and 170 MW). The line inductance is chosen to be 0.05 H. It is found from Nyquist curves that the higher the power transfer level, the more the system is prone to resonance.

The $d$-axis currents at the two power levels are shown in Fig. 17 and the active powers in the rectifier AC system at the two power levels are shown in Fig. 18. It can be observed that at 100 MW level, the ripple magnitude in $i_d$ is about 5% of the mean magnitude, while it is more than 30% at 170 MW level.
IV. CONCLUSION

Electrical resonances in a VSC-HVDC system is investigated in this paper. Frequency domain impedance models are developed for the rectifier side AC system and the inverter side AC systems. Nyquist stability analysis and impedance frequency responses are then applied to detect resonances. Impedance model based analysis demonstrates that the feed-forward filter, line inductance and level of power transfer have significant impact on resonance. A first-order filter leads to resonances with lower frequency compared to a second-order filter. Weak AC grid with long lines could lead to instability. The higher the power transfer level, the more obvious the resonances. Time-domain simulation results in Matlab/SimPowerSystems using detailed models also demonstrate the impacting factors on resonances.

APPENDIX A

TABLE I
SYSTEM PARAMETERS OF VSC-HVDC MODEL

<table>
<thead>
<tr>
<th>Quantity</th>
<th>Value</th>
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<tbody>
<tr>
<td>AC system line voltage</td>
<td>100kV</td>
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<tr>
<td>AC system frequency</td>
<td>60Hz</td>
</tr>
<tr>
<td>Grid impedance</td>
<td>0.02H/0.012H</td>
</tr>
<tr>
<td>Grid filter capacity</td>
<td>18Mvar</td>
</tr>
<tr>
<td>Grid filter tuning frequency</td>
<td>1620Hz</td>
</tr>
<tr>
<td>DC rated voltage</td>
<td>250kV</td>
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<tr>
<td>DC cable parameters</td>
<td>0.0139F/km, 0.159mH/km, 0.231μF/km</td>
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<tr>
<td>DC cable length</td>
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TABLE II
PARAMETERS OF INDIVIDUAL VSC

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<tr>
<td>Switching frequency</td>
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<td>Grid filter</td>
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<td>DC capacitor</td>
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TABLE III
PARAMETERS OF CONTROLLERS

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</thead>
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<tr>
<td>Current controller</td>
<td>$k_p=50$, $k_i=100$</td>
</tr>
<tr>
<td>DC-link voltage controller</td>
<td>$k_p=0.04$, $k_i=0.2$</td>
</tr>
</tbody>
</table>

REFERENCES


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